L Number	Hits	Search Text	DB	Time stamp
-	12443	710/$.ccls.	USPAT	2002/10/23 10:36
-	261	(interrupt adj controller) and (interrupt adj register)	USPAT; US-PGPUB	2003/03/14 17:31
-	142	710/$.ccls. and ((interrupt adj controller) and (interrupt adj register))	USPAT; US-PGPUB	2002/10/22 14:53
-	65	710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register))	USPAT; US-PGPUB	2002/10/23 09:00
-	10	(storage adj device) and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register)))	USPAT; US-PGPUB	2002/10/22 14:58
-	1	circuitry and ((storage adj device) and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register))))	USPAT; US-PGPUB	2002/10/22 14:59
-	64	stor$4 and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register)))	USPAT; US-PGPUB	2002/10/22 19:33
-	44	stor$4 and hardware and software and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register)))	USPAT; US-PGPUB	2002/10/23 10:57
-	1	("5459872").PN.	USPAT; US-PGPUB	2002/10/23 08:58
-	1	stor$4 and 6298410.PN.	USPAT; US-PGPUB	2002/10/23 08:58
-	1	reserv$4 and stor$4 and (("5459872").PN.)	USPAT; US-PGPUB	2002/10/23 08:58
-	261	((interrupt adj controller) and (interrupt adj register))	USPAT; US-PGPUB	2002/10/23 09:00
-	71	((interrupt adj controller) and (interrupt adj register)) and (stor$4 adj device)	USPAT; US-PGPUB	2002/10/23 09:02
-	0	((interrupt adj controller) and (interrupt adj register)) and (stor$4 adj device) and (distinguesh$3 near6 requests)	USPAT; US-PGPUB	2002/10/23 09:02
-	0	((interrupt adj controller) and (interrupt adj register)) and (stor$4 adj device) and (distinguish$3 near6 requests)	USPAT; US-PGPUB	2002/10/23 09:03
-	0	((interrupt adj controller) and (interrupt adj register)) and (stor$4 adj device) and (distinguish$3 adj2 requests)	USPAT; US-PGPUB	2002/10/23 09:03
-	65	((interrupt adj controller) and (interrupt adj register)) and (stor$4 adj device) and (hardware and software)	USPAT; US-PGPUB	2002/10/23 09:03
-	0	((interrupt adj controller) and (interrupt adj register)) and (stor$4 adj device) and ((hardware and software) near6 request)	USPAT; US-PGPUB	2002/10/23 09:04
-	19	((interrupt adj controller) and (interrupt adj register)) and (stor$4 adj device) and ((hardware and software) same request)	USPAT; US-PGPUB	2002/10/23 09:27
-	88	((interrupt adj controller) and (interrupt adj register)) and (hardware and software) and (stor$4 near6 request)	USPAT; US-PGPUB	2002/10/23 09:30
-	0	((interrupt adj controller) and (interrupt adj register)) and ((hardware and software) near6 request) and (stor$4 near6 request)	USPAT; US-PGPUB	2002/10/23 09:30
-	35	((interrupt adj controller) and (interrupt adj register)) and ((hardware and software) same request) and (stor$4 near6 request)	USPAT; US-PGPUB	2002/10/23 10:33
-	219	(hardware adj interrupt) same (software adj interrupt)	USPAT; US-PGPUB	2003/03/14 16:55
-	0	(hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1) and 710/260.ccls	USPAT; US-PGPUB	2002/10/23 10:35
-	0	(hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1) and 710/260-266.ccls	USPAT; US-PGPUB	2002/10/23 10:35

–	297	(hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1)	USPAT; US-PGPUB	2003/03/14 18:13
–	42	710/260-266.ccls. and ((hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1))	USPAT	2002/10/25 16:18
–	44	stor$4 and hardware and software and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register)))	USPAT; US-PGPUB	2002/10/23 10:57
–	18825	latch adj circuit	USPAT; US-PGPUB	2002/10/25 16:18
–	35	710/260-266.ccls. and ((hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1))	USPAT	2002/10/25 16:18
–	46	710/260-266.ccls. and ((hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1))	USPAT; US-PGPUB	2002/10/25 16:19
–	0	(latch adj circuit) and (710/260-266.ccls. and ((hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1)))	USPAT; US-PGPUB	2002/10/25 16:19
–	6	(latch near6 circuit) and (710/260-266.ccls. and ((hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1)))	USPAT; US-PGPUB	2003/03/14 18:12
–	1	("6397284").PN.	USPAT; US-PGPUB	2002/10/28 08:45
–	15	(hardware adj interrupt) same (software adj interrupt) and ((permanent$3 fix$3) near5 assign$4)	USPAT; US-PGPUB	2003/03/14 17:04
–	0	(hardware adj interrupt) same (software adj interrupt) same ((permanent$3 fix$3) near5 assign$4)	USPAT; US-PGPUB	2003/03/14 17:00
–	0	(hardware adj interrupt) same (software adj interrupt) same ((permanent$3 fix$3) same assign$4)	USPAT; US-PGPUB	2003/03/14 17:00
–	1	(hardware adj interrupt) same ((permanent$3 fix$3) near5 assign$4)	USPAT; US-PGPUB	2003/03/14 17:06
–	3	(hardware adj interrupt) same ((permanent$3 fix$3) same assign$4)	USPAT; US-PGPUB	2003/03/14 17:06
–	4	(interrupt adj controller) and (interrupt adj register) and (select$3 assign$4 choos$3) near10 (equal adj priority)	USPAT; US-PGPUB	2003/03/14 17:36
–	119	(interrupt adj controller) and (interrupt adj register) and ((select$3 assign$4 choos$3) near10 priority)	USPAT; US-PGPUB	2003/03/14 18:10
–	0	(interrupt adj controller) and (interrupt adj register) and (select$3 assign$4 choos$3) near10 (between adj priority)	USPAT; US-PGPUB	2003/03/14 17:37
–	20	(interrupt adj controller) and (interrupt adj register) and ((select$3 assign$4 choos$3) near10 priority) and (round adj robin)	USPAT; US-PGPUB	2003/03/14 18:12
–	0	(latch near6 circuit) and (710/260-266.ccls. and ((hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1))) same (round adj robin)	USPAT; US-PGPUB	2003/03/14 18:13
–	69	(hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1) and (round adj robin)	USPAT; US-PGPUB	2003/03/14 18:14
–	11	(hardware near5 interrupt) same (software near5 interrupt) same (reg$1 or register$1) and (round adj robin) and (equal adj priority)	USPAT; US-PGPUB	2003/03/14 18:14